

REMARKS

Claims 1-41 are pending in the application. Claims 1 and 23 have been amended. Claims 40 and 41 are newly submitted. Fig. 2 has been amended to correspond to the specification. No new matter has been added. Reconsideration is respectfully requested in view of the amendments to the claims and the following remarks.

I. Double Patenting

Claims 1-8 and 11-18 were rejected under the judicially created doctrine of obviousness-type double patenting over claims 1-16 of U.S. Patent No. 6,529,056.

Claims 9-10 and 19-22 were rejected under the judicially created doctrine of obviousness-type double patenting over claims 1-16 of U.S. Patent No. 6,529,056 in view of U.S. Patent No. 6,747,505 ("Gergintschew") or U.S. Patent No. 6,703,895 ("Khemka").

Claims 23-36 were rejected under the judicially created doctrine of obviousness-type double patenting over claims 1-14 of U.S. Patent No. 6,703,888.

Claims 37-39 were rejected under the judicially created doctrine of obviousness-type double patenting over claims 1-14 of U.S. Patent No. 6,703,888 in view of Gergintschew or Khemka.

Claims 1-8, 11-18 and 23-36 were rejected under the judicially created doctrine of obviousness-type double patenting over claims 1-24 of U.S. Patent No. 6,433,614.

Claims 9-10, 19-22 and 37-39 were rejected under the judicially created doctrine of obviousness-type double patenting over claims 1-24 of U.S. Patent No. 6,433,614 in view of Gergintschew or Khemka.

A terminal disclaimer is enclosed for U.S. Patent Nos. 6,529,056, 6,703,888 and 6,433,614.

II. The § 102 Rejections

Claims 1-2, 6, 8-13, 18-22, 23-24, 27, 30-35 and 37-39 were rejected under 35 U.S.C. § 102(e) as being anticipated by Gergintschew.

Applicant : You et al.
Serial No. : 10/796,405
Filed : March 8, 2004
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Attorney's Docket No.: 09464-029001

Amendments to the Drawings:

The attached replacement sheet of drawings includes changes to Fig. 2 and replaces the original sheet including Fig. 2.

Fig. 2, was amended to correspond to the specification at page 2, lines 21-23. In particular, reference designations (*t3*) and (*t4*) were missing in Fig. 2 and have been added by the amendment. Support for the amendment may be found in Figure 2 of U.S. Patent 6,529,056, which was incorporated by reference.

Attachments following last page of this Amendment:

Replacement Sheet (1 page)
Annotated Sheet Showing Change(s) (1 page)

Claim 1, as amended, recites a control circuit having a first control signal coupled to a first gate of a first FET, and a second control signal coupled to a second gate of a second FET. The first control signal and the second control signal are separate control signals, and the first control signal has a rising edge with a different timing than a rising edge of the second control signal.

Gergintschew discloses a circuit configuration that includes a control device and two semiconductor switches (see Abstract). More specifically, Gergintschew discloses a control device 6 that is connected to gate terminals of two semiconductor switches 2, 3 (see Fig. 1; col. 4, ll. 1-2). Gergintschew, however, fails to disclose a control circuit having separate control signals that have rising edges at different timings, as required by claim 1. Instead, Gergintschew discloses that the gate terminals of the two semiconductor switches 2, 3 are connected to each other (col. 4, ll. 5-7). Because the gate terminals of semiconductor switches 2, 3 are connected to each other, a control signal for the two semiconductor switches will, therefore, have a rising edge with a same timing. In contrast, the control circuit as recited in claim 1 has separate control signals that have rising edges at different timings. Claim 1 is, therefore, allowable over Gergintschew.

Claims 2-22 and 40 depend from claim 1, and are allowable over Gergintschew for at least the same reasons as set forth with claim 1.

Claim 23 recites limitations similar to claim 1. Claim 23 (and the claims that depend therefrom) are also allowable over Gergintschew for at least the same reasons as set forth with claim 1.

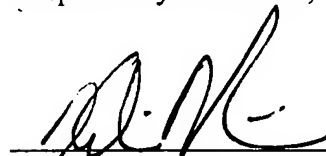
Enclosed is a \$50 check for excess claim fees. Please apply any other charges or credits to deposit account 06-1050.

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Respectfully submitted,

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You et al.

"Method And Apparatus For Operating A Field-Effect Transistor (FET) Pair"

ANNOTATED SHEET

FIG. 2

